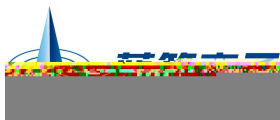


[illegible]



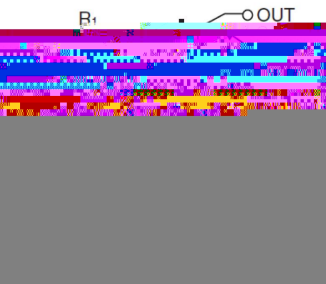
/ Descriptions

SOT-23 PNP Silicon PNP Digital transistor in a SOT-23 Plastic Package.

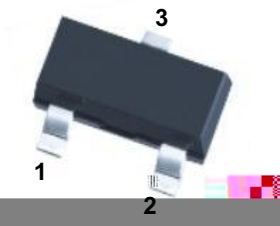
/ Features

/ Applications

/ Equivalent Circuit

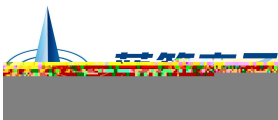


/ Pinning

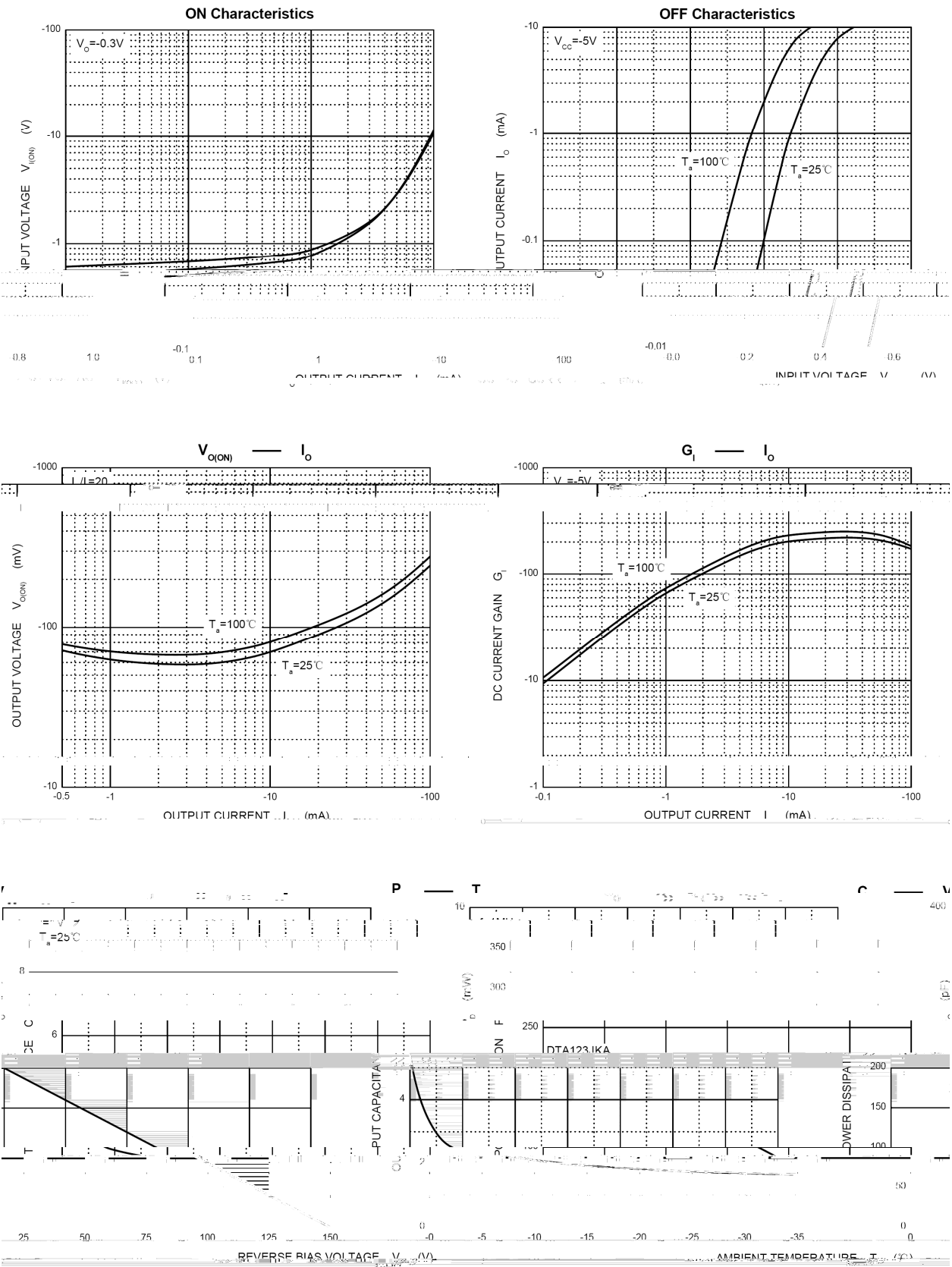


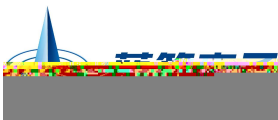
/ Marking

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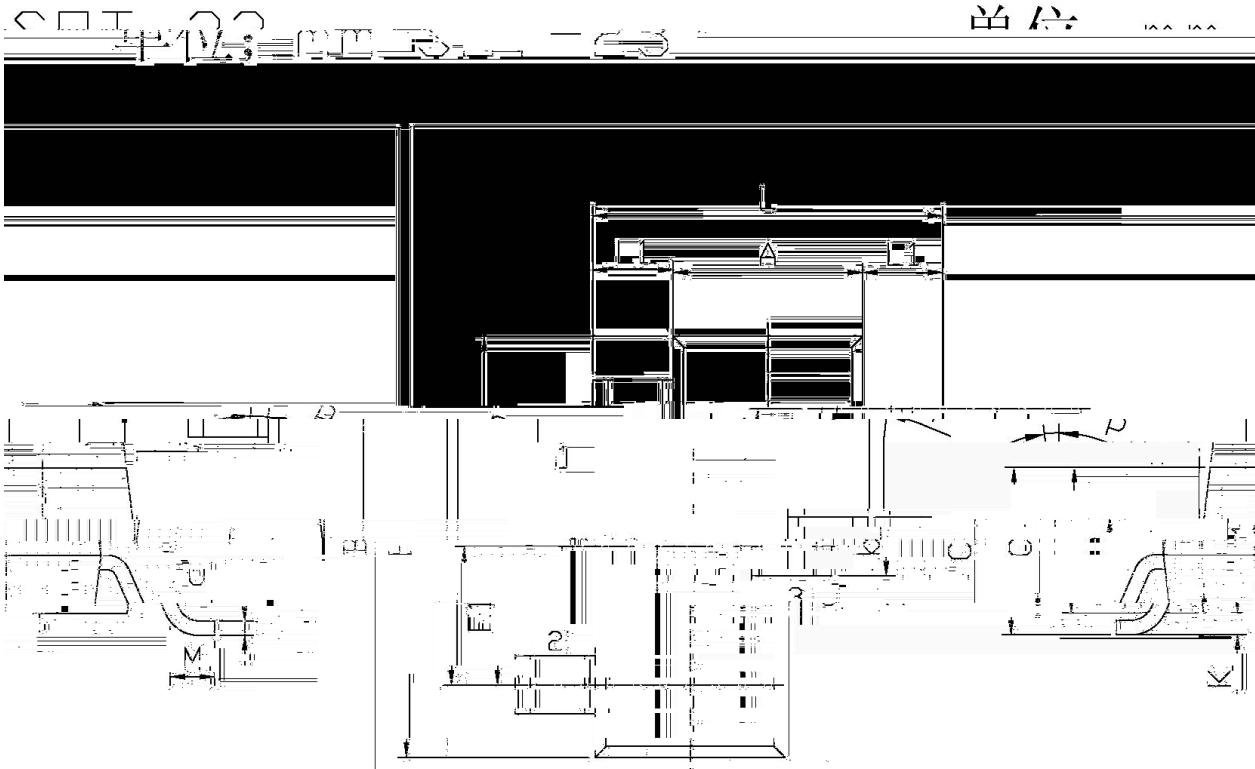


/ Electrical Characteristic Curve

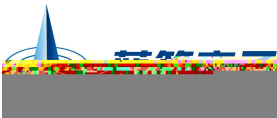




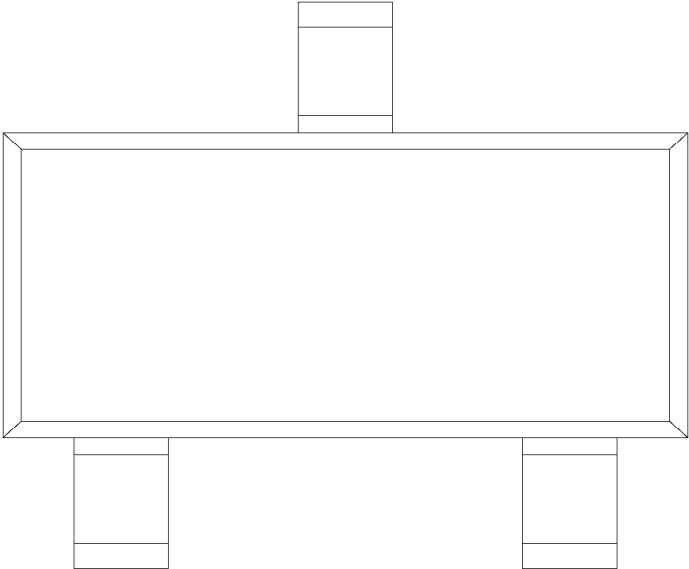
/ Package Dimensions



Symbol	Dimensions In Millimeters		Symbol	Dimensions In Millimeters	
	Min	Max		Min	Max
L	2.2	2.7	C	1.30Max	
A	0.45	0.65	C1	0.90	1.20
B	1.15	1.50	C2	0.05	0.20
D	2.70	3.10	K	0.10	0.10
E	0.85	1.05	P	0.35	0.55
F	0.35	0.55			

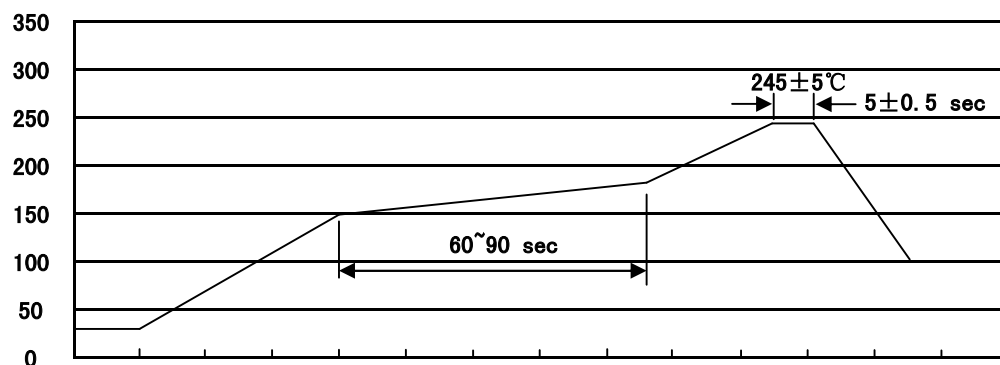


/ Marking Instructions



H

H


$$\begin{array}{ccccccc} & & & & & & ^\circ\text{C} \\ \pm & & & \pm & & \pm & ^\circ\text{C} \\ & & ^\circ\text{C} & & & & \\ & & & & & ^\circ\text{C} & \pm \end{array}$$

/ Resistance to Soldering Heat Test Conditions

$$\pm \text{ }^{\circ}\text{C} \qquad \qquad \qquad \pm \qquad \qquad \qquad \text{ }^{\circ}\text{C}$$
/ **Packaging SPEC.**

封装形式	包装数量					包装尺寸		
	只 卷盘	卷盘 盒	只 盒	盒 箱	只 箱		盒	箱
						"		

/ Notices